

$$\begin{array}{r} 7601 \\ \hline 925 \end{array}$$

U.S. UTILITY Patent Application

APPL NUM 10044910	FILING DATE 01/15/2002	CLASS 361	SUBCLASS 793	GAU 282 2000	EXAMINER 111
**APPLICANTS: Okabe Hiroshi; Yamada Hirozi;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-047546 02/23/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO XA-9611	
TITLE : Electronic circuit equipment using multilayer circuit board					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims		Print Claim for O.G
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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